

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Rutten, Ivo

Atty. Docket No.: US 018205

Serial No.:

Group Art Unit:

Filed:

Examiner:

Title: MICROPROCESSOR-BASED PROBE FOR INTEGRATED CIRCUIT TESTING

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

Sir:

Enclosed is an Information Disclosure Citation and copies of documents listed thereon.

These documents are considered to be relevant in that they have been:

considered in drafting the specification of the above-referenced application;

cited in the specification of the above-referenced application; or,

cited as an "X" or "Y" document in a foreign Patent Office search report on a foreign counterpart application, a copy of which report is also enclosed;

I hereby certify that these documents were cited in said search report not more than three (3) months ago.

Please charge any fee under 1.17(p) for this Information Disclosure Statement to be considered, not exceeding \$, to Deposit Account No. .

Respectfully submitted,

Robert M. McDermott, Esq.

Reg. No. 41,508 804-493-0707

CERTIFICATE OF MAILING

It is hereby certified that this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to: COMMISSIONER OF PATENTS AND TRADEMARKS. Washington, D.C. 20231

on 18 Dec 01